

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

JOSEPH F. GARVEY

Serial No. **Unknown**

Filed: **Herewith**

For: **OPTIMAL CODE GENERATION
FOR STRUCTURED ASSEMBLY
LANGUAGE EXPRESSIONS**

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Attorney Docket No. **RAL920000124US1**

Examiner: **Unknown**

Group Art Unit: **Unknown**

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10-17-01
11050 U.S. PTO
09/939378
08/24/01

**INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 C.F.R. § 1.97(b)**

Assistant Commissioner for Patents
Washington, D. C. 20231

Sir:

Applicant requests that the information listed on the attached Substitute Form PTO-1449 be considered by the Office during the pendency of the above-entitled application, pursuant to 37 C.F.R. 1.97. This Information Disclosure Statement is being filed under 37 C.F.R. § 1.97(b) within three months of the filing date of the application, or before the mailing date of a first office action on the merits. No fee or certification is required.

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Date of Deposit: August 24, 2001

I hereby certify that this paper and fee are being deposited with the United States Postal Service Express Mail Post Office to Addressee service under 37 CFR Sec. 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Karen Orzechowski

Name of Person Mailing Paper and Fee

Karen Orzechowski

Signature of Person Mailing Paper and Fee

No fee is believed to be necessary; however, in the event that any additional fee is required, please charge it to IBM Deposit Account Number 09-0464.

Respectfully submitted,



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| Substitute form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT | | | | Complete if Known | | |
| | | | | Application Number | Unknown | |
| | | | | Filing Date | Herewith | |
| | | | | First Named Inventor | JOSEPH F. GARVEY | |
| | | | | Group Art Unit | Unknown | |
| | | | | Examiner Name | Unknown | |
| Sheet | 1 | of | 1 | Attorney Docket Number | RAL920000124US1 | |
| U.S. PATENT DOCUMENTS | | | | | | |
| Examiner Initials | IDS Doc # | U.S. Patent Document Number | Name of Patentee or Applicant | | Date of Publication MM-DD-YYYY | |
| | AA | 6,061,514 | Michiaki Kuno | | 11-09-2000 | |
| FOREIGN PATENT DOCUMENTS | | | | | | |
| Examiner Initials | IDS Doc # | Office | Foreign Patent Document Number | Name of Patentee or Applicant | Date of Publication | English Translation |
| OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS | | | | | | |
| Examiner Initials | IDS Doc # | Include the name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published | | | English Translation | |
| | AB | Analysis of Complex Assembler Programs, IBM TECHNICAL DISCLOSURE BULLETIN, November 1991, pp. 98-100. | | | Yes | |
| | AC | Functional Binary Assembler, IBM TECHNICAL DISCLOSURE BULLETIN, October 1991, pp. 259-261. | | | Yes | |
| | AD | Assembler Macro Implementation, IBM TECHNICAL DISCLOSURE BULLETIN, May 1994, pp. 19-28. | | | Yes | |
| | AE | General Purpose Assembler for Microprocessors, IBM TECHNICAL DISCLOSURE BULLETIN, January 1979, pp. 3356-3357. | | | Yes | |
| | AF | Process to Convert Assembler Language Programs to Decision Tables, IBM TECHNICAL DISCLOSURE BULLETIN, December 1977, pp. 2654-2658. | | | Yes | |
| | AG | Fast Assembler using APL, IBM TECHNICAL DISCLOSURE BULLETIN, June 1977, pp. 368-372. | | | Yes | |
| Examiner Signature | | | | | Date Considered | |

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

